## PATENT ABSTRACTS OF JAPAN

(11)Publication number:

11-068770

(43) Date of publication of application: 09.03.1999

(51)Int.Cl.

H04L 12/28 H04Q 3/00

(21) Application number: **09-227193** 

(71)Applicant: NEC CORP

(22)Date of filing:

08.08.1997

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## (54) SCHEDULING SYSTEM FOR ATM SWITCH

## (57) Abstract:

PROBLEM TO BE SOLVED: To provide a scheduling system for an asynchronous transfer mode ATM switch in which deterioration in a delay characteristic of traffic is reduced, where real time performance is a requirement. SOLUTION: Low or high priority is set to each traffic class and high priority is placed on a traffic class where a real time performance is a requirement and an output cell selection section 5 decides a queue whose output is allowed by the rotation priority system among queues 31-3n in the case that a class whose priority is placed high among classes where product between counts of weight counters 41-4n of each class and queue length of the queues 31-3n are not zero. In the case that a class whose priority is placed high is not in existence, scheduling control in an ATM switch is realized by deciding a queue whose output is allowed by the rotation priority system among the classes whose priority is placed low.

